

Serial No.: 09/526,646

Filed: March 15, 2000

Title: AUTOMATIC DATA CPU LOAD REDUCTION IN A HOST-SIGNAL PROCESSING (HSP) BASED ADSL MODEM

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**REMARKS**

This responds to the Office Action mailed on November 16, 2005.

Claim 50 is amended, and claim 49 is canceled herein; as a result, claims 1-3, 5-11, 13-19, 21-48, 50, and 51 are now pending in this application.

**§102 Rejection of the Claims**

Claim 49 was rejected under 35 U.S.C. § 102(e) for anticipation by Chellali et al. (US 6,201,830.)

Claim 49 is cancelled without prejudice.

**Allowable Subject Matter**

Claims 1-3, 5-11, 13-19 and 21-48 have been allowed.

Claims 50-51 were objected to as being dependent upon a rejected base claim, but were indicated to be allowable if rewritten in independent form including all of the limitations of the base claim and any intervening claims.

Claim 50 is amended to include the limitations of claim 49. (Claim 51 depends upon claim 50.)

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**CONCLUSION**

Applicant respectfully submits that the claims are in condition for allowance and notification to that effect is earnestly requested. The Examiner is invited to telephone Applicant's attorney 612-373-6900 to facilitate prosecution of this application.

If necessary, please charge any additional fees or credit overpayment to Deposit Account No. 19-0743.

Respectfully submitted,

MING-KANG LIU

By his Representatives,

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Date 2-14-06

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Date of Deposit: <sup>14, ch</sup> February 13, 2006

This paper or fee is being filed on the date indicated above using the USPTO's electronic filing system EFS-Web, and is addressed to: The Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

CANDIS BUENDING  
Name

Candis Buending  
Signature